

Architecture

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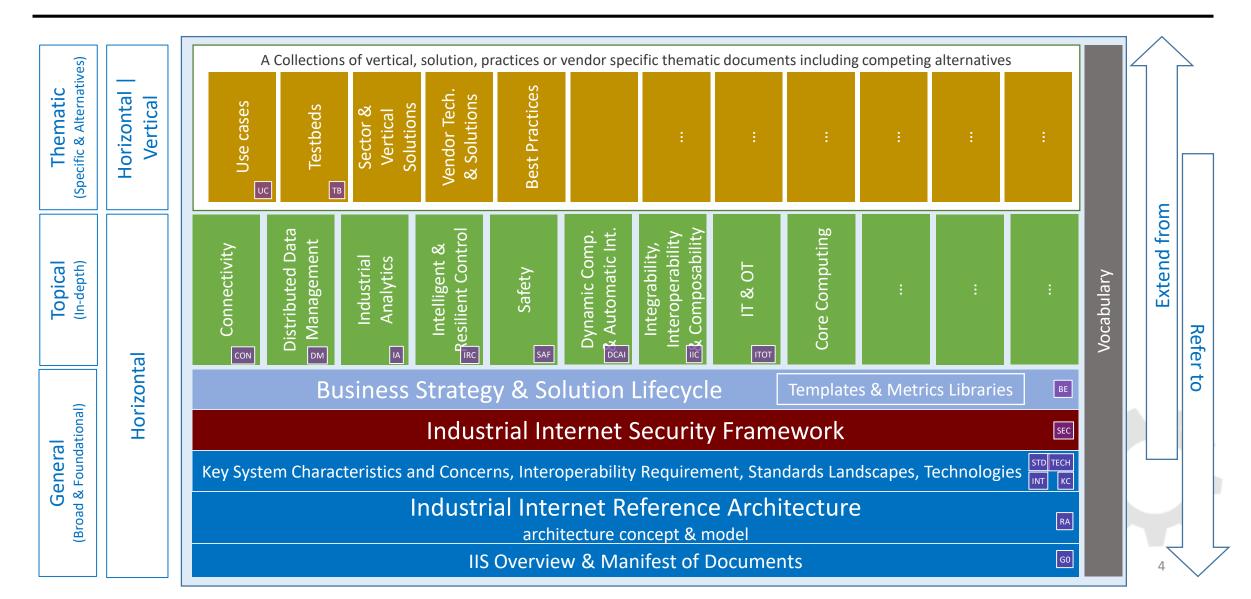


The Architecture Task Group is responsible for the creation, evolution and adoption of the Industrial Internet Reference Architecture (IIRA) and its supporting materials to enable open and interoperable IIoT technologies



- Identify and document major IIoT architecture concerns and develop systematic <u>architecture frameworks and methodologies</u> for their resolution;
- Identify and elaborate *common IIoT architecture patterns*
- Identify and evaluate standards, practices, and technologies best suited for building IIoT systems, and highlight gaps where needed.
- Identify and document architecture elements and development activities that guide the realization and evaluation of IIoT key system characteristics.
- <u>Engage with testbed teams and other adopters</u> to leverage IIRA successfully and incorporate their feedback into future versions.
- Manage the author and publication lifecycle of the IIRA, and coordinate with other teams to <u>ensure consistency</u> between IIRA and other related artifacts in both structure and content.







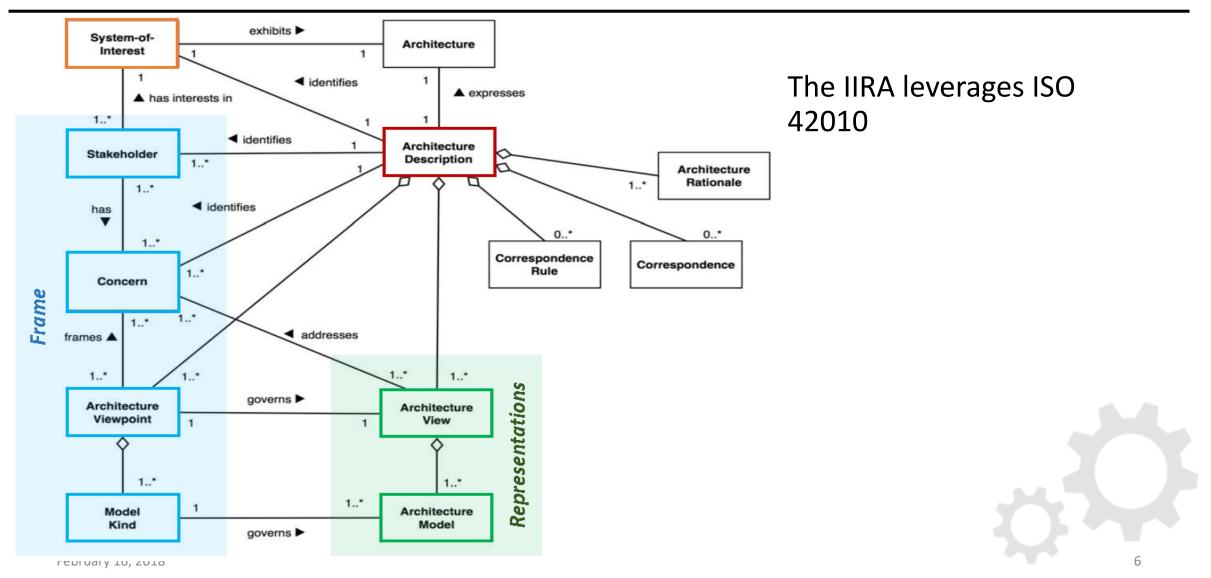
What it is not:

An Architecture

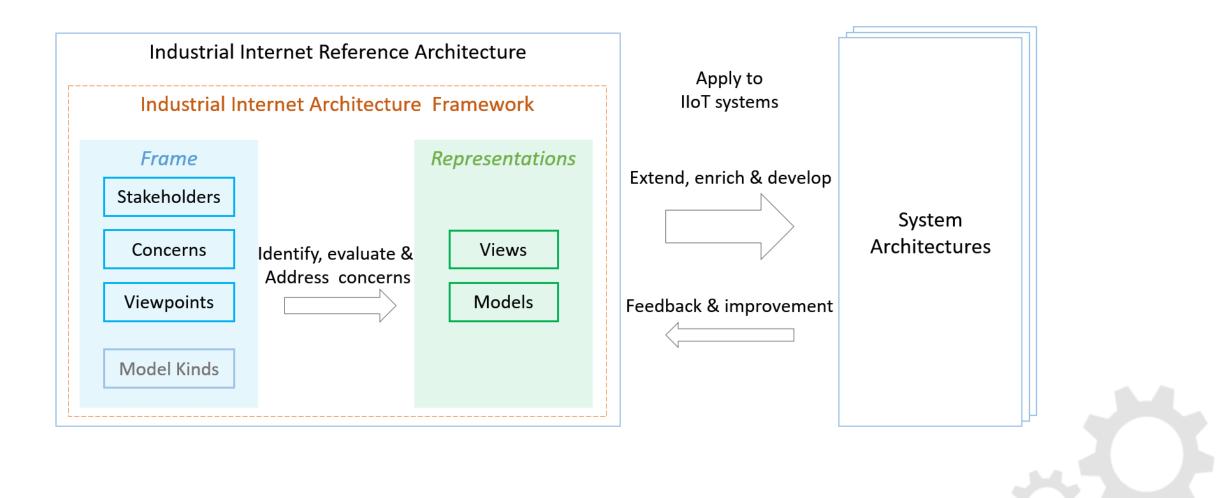
What it is:

A methodology that enables system architects to use standards based common approaches, common vocabularies, and common representations to design an optimal, interoperable IIoT system for their unique requirements.

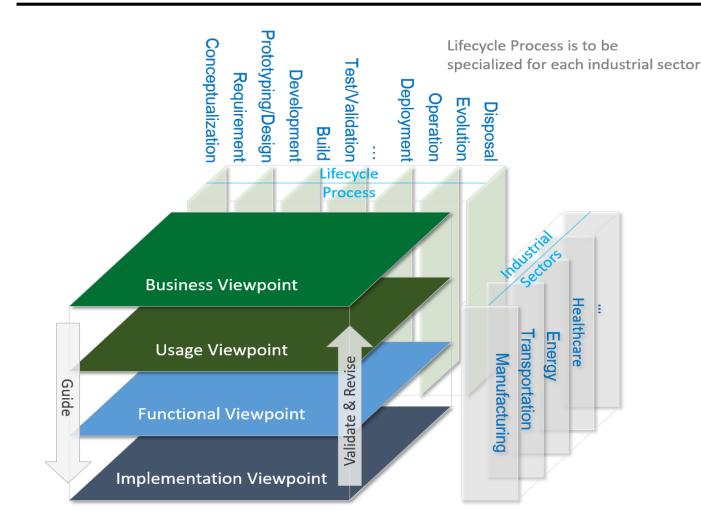
Basis: ISO/IEC/IEEE 42010:2011 – Systems & Software Engineering -- Architecture Description



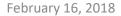








- Viewpoints address specific stakeholder requirements
- Viewpoints are sequential
- Viewpoints are multidirectional
- Viewpoints address Lifecycle process
- Viewpoints enable view creation regardless of industrial sector

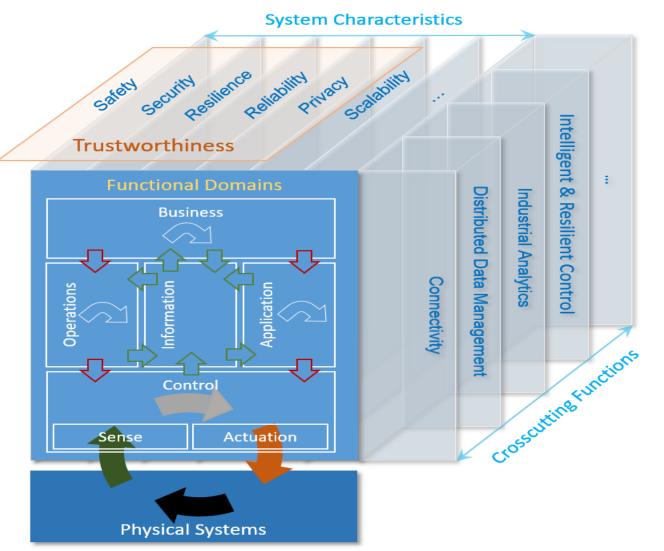


Functional View – Domains and Cross Cutting Functions

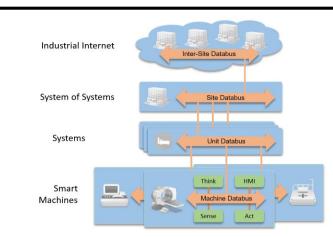
A *functional domain* is a (mostly) distinct functionality in the overall IIoT system

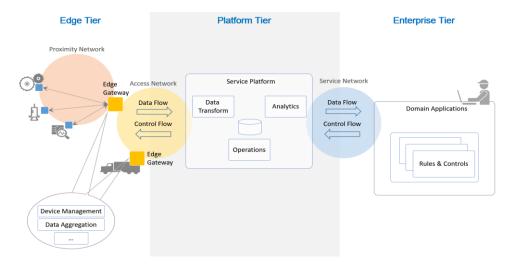
IIRA Functional View definesFive Domains – Control,Operations, Information,Application, Business

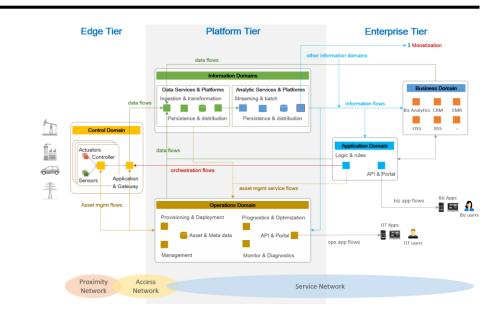
Additional functions must be provided to enable the major system functions

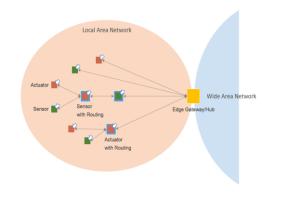


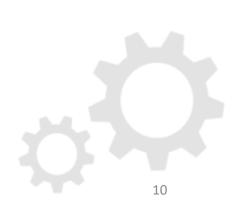




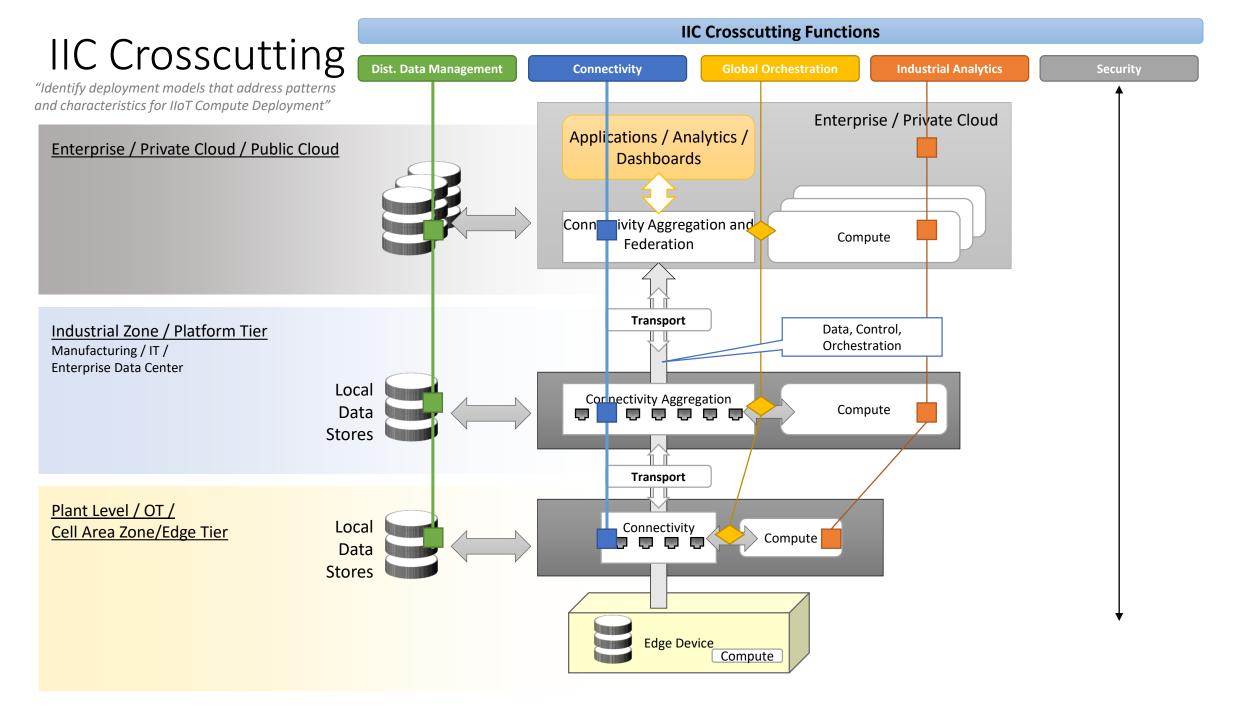




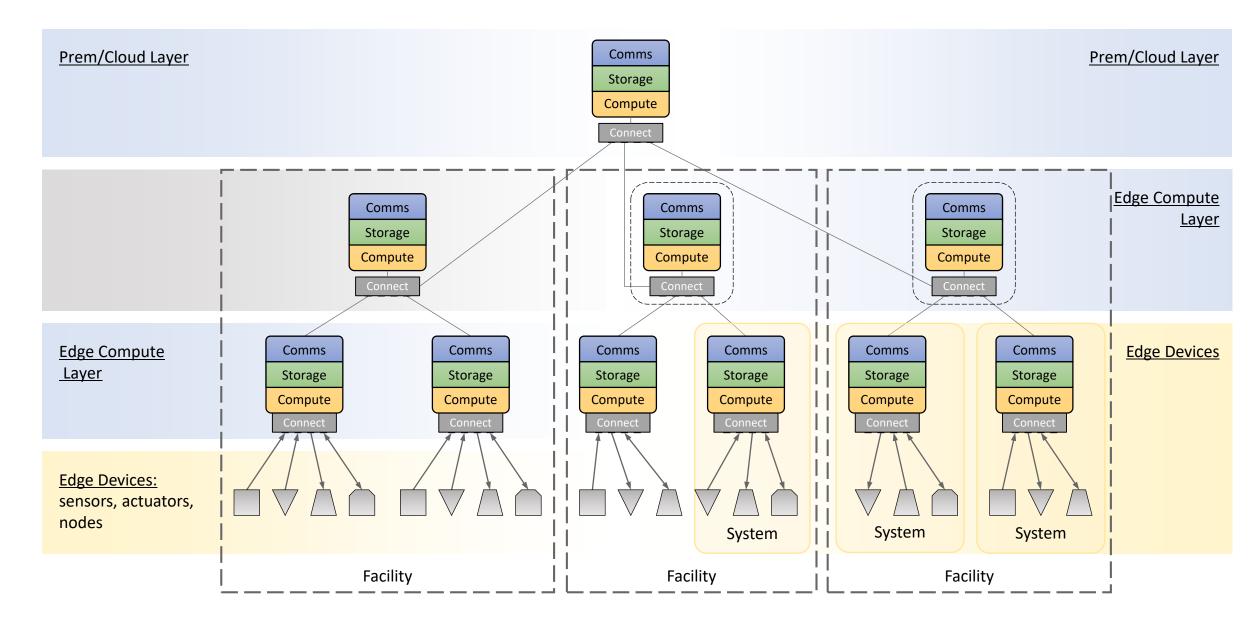




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Edge/Edge Computing Definition Diagram





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